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| International Application Number: | | | | | |
| Confirmation Number: | 1070 | | | | |
| Title of Invention: | CODING OF FPGA AND STANDARD CELL LOGIC IN A TILING STRUCTURE | | | | |
| First Named Inventor/Applicant Name: | Stanislav Peter Bajuk | | | | |
| Correspondence Address: | DILLON & YUDELL LLP - 8911 NORTH CAPITAL OF TEXAS HIGHWAY SUITE 2110 AUSTIN TX 78759 US 512-343-6446 - | | | | |
| Filer: | Brian Franklin Russell./Jennifer Kammerdiener | | | | |
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.